SJ-ETHER/SJ Series
Analog Module

■Analog Output Module《4 Channels》

C0-04DA-2

Output Specifications

Items	Specifications
Outputs per Module	4
Output Range	0 to 10 V
Resolution	12-bit, 2.44 mV/count
Output Type	Voltage sourcing at 10mA maximum (One common)
Output Value in Program Mode	Determined by CPU
Output Value in Fault Mode	0 V
Output Impedance	0.2 Ω typical
Load Impedance	> 1,000 Ω
Maximum Capacitive Load	0.01 uF maximum
Allowed Load Type	Grounded
Maximum Inaccuracy	0.5% of range
Maximum Full Scale Error (Not including Offset)	±0.2% of range maximum voltage
Maximum Offset Error	±0.2% of range maximum
Accuracy Against Temperature	±75 PPM/℃ maximum full scale calibration change (±0.0025% of range/℃)
Maximum Crosstalk at DC, 50/60 Hz	-72 dB, 1 LSB
Linearity Error (End-to-end)	±4 LSB maximum (±0.1% of full scale); monotonic with no missing codes
Output Stability and Repeatability	±2% LSB after 10 minute warmup period typical
Output Ripple	±0.1% of full scale
Output Settling Time	0.3 ms maximum, 5 μs minimum (Full scale range)
All Channel Update Rate	10 ms
Maximum Continuous Overload	Outputs current limited to 40 mA typical; continuous overloads on multiple outputs can damage module.
Field to Logic Side Isolation	1,800 V AC applied for 1 second (100% tested)
Type of Output Protection	0.1 μF transient suppressor
Output Signal at Power Up and Power Down	0 V
External 24 V DC Power Required	85 mA
Bus Power Required (24 VDC)	20 mA
Terminal Block Replacement	ADC p/n C0-8TB
Weight	82 g

Wiring Diagram

The specifications and prices described in this catalog were valid when the catalog was issued. For the latest information, contact our sales persons or see our website.

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