

SJ-ETHER/SJ Series

Analog Module

PLC	
HMI	
SENSOR	
ENCODER	
COUNTER	
INFORMATION	

Common Subject Matter

SJ-ETHER/SJ

DL05/06

DL205

D4

D3

Programmer

KPP

DirectSOFT

Terminator I/O

Features

Specifications

Dimensions

CPU Specifications

Power Supply Module

Input/Output Module

Analog Module

Analog Input/Output Module 《Input 4 Channels•Output 2 Channels》

C0-4AD2DA-2



General Specifications

Items	Specifications
Field to Logic Side Isolation	1,800 V AC
External 24 VDC Power Required	65 mA
Bus Power Required (24 V DC)	15 mA
Terminal Block Replacement	ADC p/n C0-16TB
Weight	86 g

Input Specifications

Items	Specifications
Inputs per Module	4
Input Range	0 to 10 V
Resolution	13-bit, 1.22 mV/count
Input Type	Single ended (One common)
Maximum Continuous Overload	±100 V DC
Input Impedance	> 150 kΩ
Filter Characteristics	Low pass, -3 dB at 500 Hz
Sample Duration Time	5 ms
All Channel Update Rate	20 ms
Open Circuit Detection Time	Zero reading within 100 ms
Conversion Method	Successive approximation
Accuracy Against Temperature	Maximum ±75 ppm/°C
Maximum Inaccuracy	0.5% of range (including temperature changes)
Linearity Error (End-to-end)	±3 count maximum, monotonic with no missing codes
Input Stability and Repeatability	Maximum ±2 count
Full Scale Error (Including Offset)	Maximum ±8 count
Offset Error	Maximum ±8 count
Maximum Crosstalk at DC, 50/60 Hz	Maximum ±2 count

Output Specifications

Items	Specifications
Outputs per Module	2
Output Range	0 to 10 V
Resolution	12-bit, 2.44 mV/count
Output Type	Voltage sourcing at 10mA maximum (One common)
Output Value in Program Mode	Determined by CPU
Output Value in Fault Mode	0 V
Output Impedance	0.2 Ω typical
Load Impedance	> 1,000 Ω
Maximum Capacitive Load	0.01 μF maximum
Allowed Load Type	Grounded
Maximum Inaccuracy	±1% of range
Maximum Full Scale Error (Not including Offset)	±0.2% of range maximum voltage

Maximum Offset Error	±0.2% of range maximum
Accuracy Against Temperature	±75 ppm/°C maximum full scale calibration change (±0.0025% of range/°C)
Maximum Crosstalk at DC, 50/60 Hz	-72 dB, 1 LSB
Linearity Error (End-to-end)	±4 LSB maximum, (±0.1% of full scale), monotonic with no missing codes
Output Stability and Repeatability	±2% LSB after 10 minute warmup period typical
Output Ripple	0.5% of full scale
Output Settling Time	0.3 ms maximum, 5 μs minimum (Full scale range)
All Channel Update Rate	20 ms
Maximum Continuous Overload	Outputs current limited to 40 mA typical; continuous overloads on multiple outputs can damage module.
Type of Output Protection	0.1 μF transient suppressor
Output Signal at Power Up and Power Down	4 mA

Wiring Diagram

